计算机组成原理 习题解答

<http://www.docin.com/p-1754035542.html>

第5章 部分习题解答

5.4-5.8,5.15-5.18

5.4 （第4版5.5）

5.4.1 L1=>write buffer=>L2 =>write-back buffer

5.4.2 参阅第5版5.5.2

（1）For L1 write miss, issue write miss request to the L2cache

(2) If hit in L2, write in L2

(3)If miss in L2, allocate cache block of L2 for the missing data, select a replacement victim; if victim dirty, put it into the write back buffer, which will be further forwarded into memory; if L2 victim clean, put it into a victim buffer between L2 and memory

5.4.3 参阅第5版5.5.3















